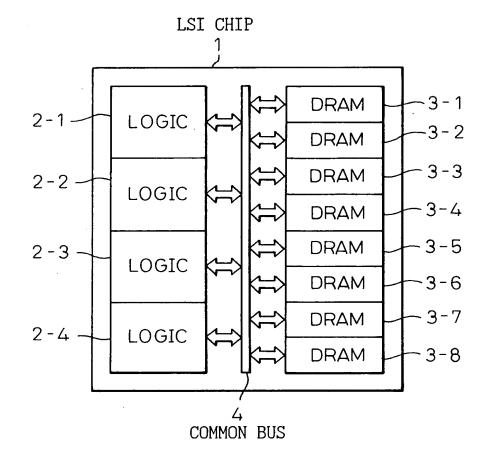


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Fig. 1 PRIOR ART



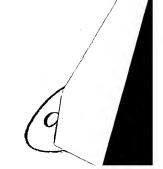
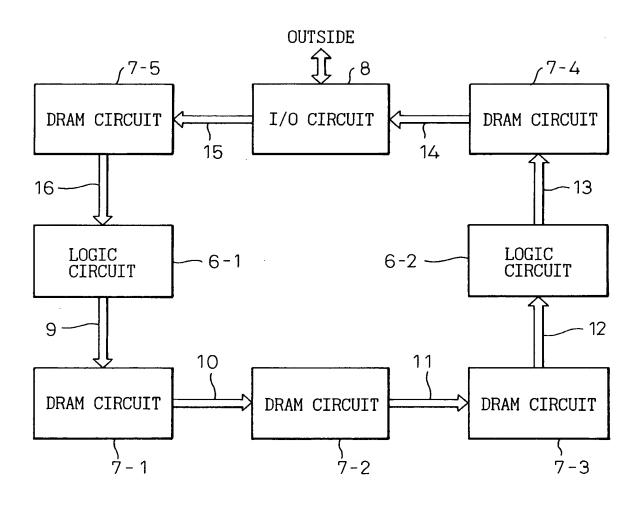


Fig.2



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CLASS SUBCLASS

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Fig.3

COMMAND-RELATED SIGNAL LINES

COMMAND FLAG LINE (ORIGINATOR ID)
DESTINATION ID)

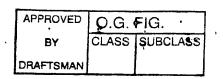
COMMAND LINE

ADDRESS LINE

DATA-RELATED SIGNAL LINES

DATA FLAG LINE (ORIGINATOR ID)

DATA LINE



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Fig. 4

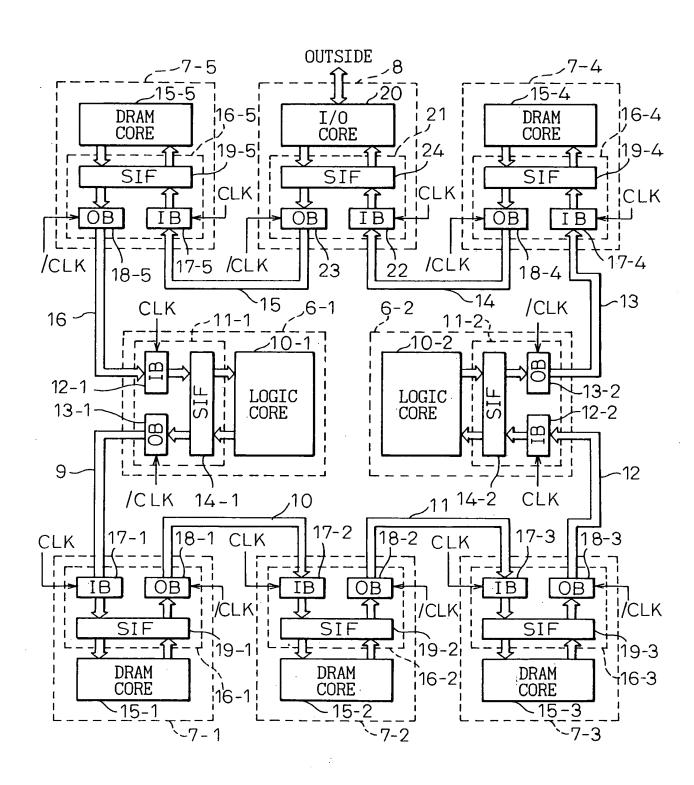
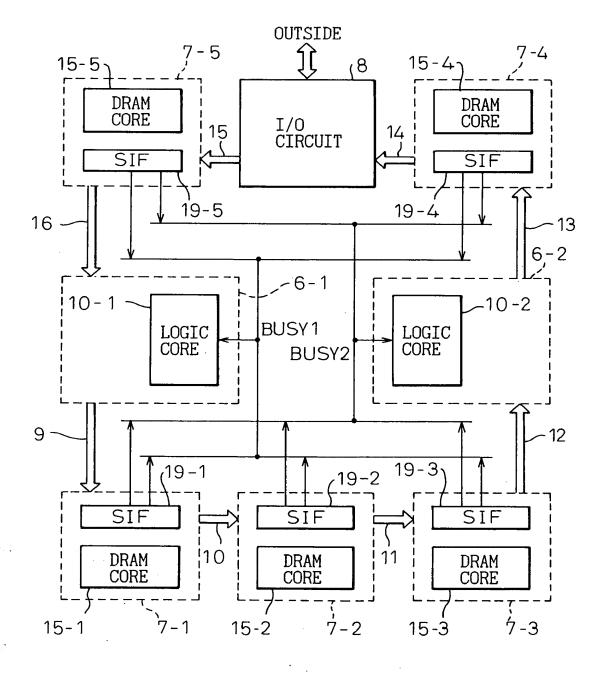


Fig.5



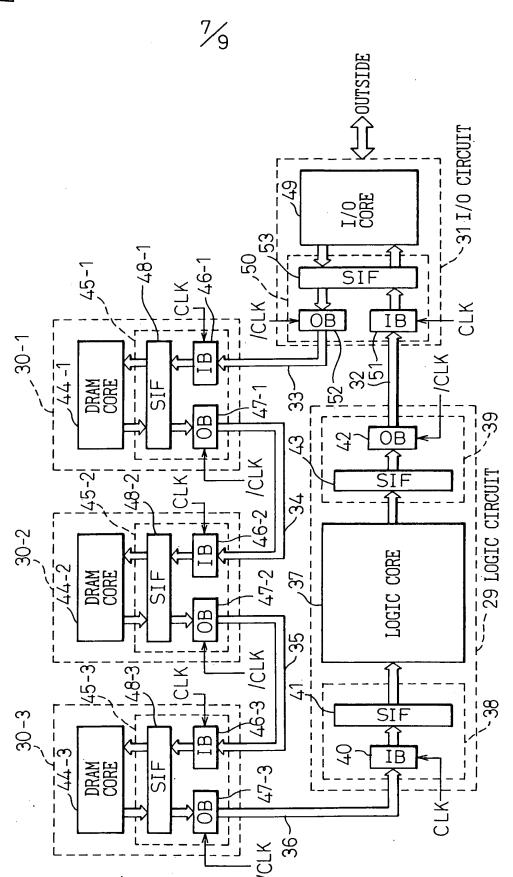
⁶⁄9 TO OUTSIDE I/O CIRCUIT 27 6-2 7-4 7-3 DRAM CIRCUIT DRAM CIRCUIT LOGIC 13/ 12/ 14 17-2 .26 DRAM CIRCUIT I/O CIRCUIT LOGIC CIRCUIT OUTSIDE <u>1</u>5 9 7-1 6-1 7-5 DRAM CIRCUIT DRAM CIRCUIT LOGIC CIRCUIT 9 16~

D9372166.091199

Fig.6

Fig.7

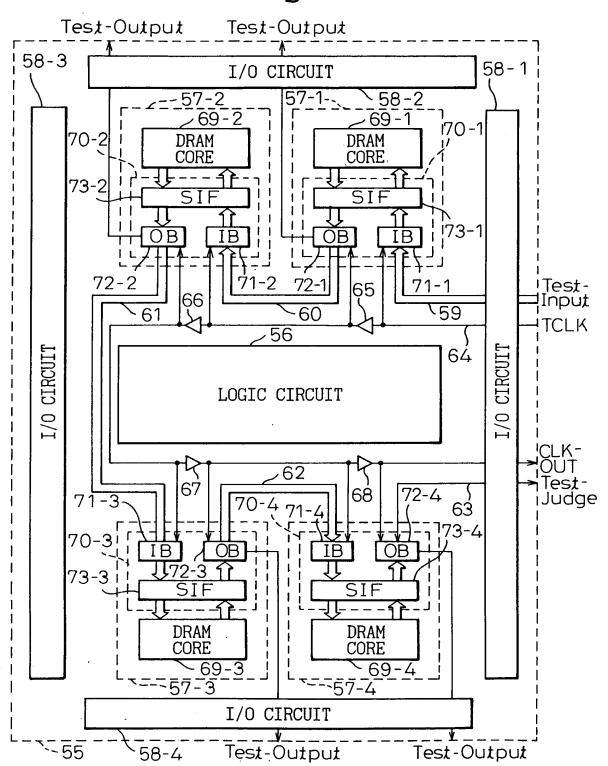
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APPROVED	O.G. FIG.		
BY	CLASS	SÚBCLAS S	
DRAFTSMAN			

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Fig.8



APPROVED	O.G. FIG.		
BY	CLASS	SUBCLASS	
DRAFTSMAN			

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Fig.9

